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PAGE 1/6 * RCVD AT 10/7/2005 2:09:49 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-6/1 * DNIS:8729306 * CSID:5109233360 * DURATION (mm-ss):01-56

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Fremont, CA, Oct. 7, 2035

Jennifer M. Dolan/2313

Examines/Art Unit

New Filt Chip Assemblies and Lamps of High Power GaN LEDs, Wafer Lovel Flip Chip Package Process, and Method of Fabricating the Same

Hui Peng

Applicants: Art unit:

2813

0.765,346 726,2:304

Application No.:

Filing date:

Reference to:

AMENDMENTA

Commissioner for Patents

P.O. Box 3450

Alexandria, Virginia 22315-1459

This letter responses to the Office Action Summary. Please amend the above patent application as the following.

TITLE: Amendment to the title of the above patent application is on page 2 of this emendarent.

SPECIFICATION: There is no amendment to the specification.

CLAIMS. Amerianent to the claims of the above patent application begin on page 3 of

this amendment.

DRAWINGS: There is no amendment to the drawings.

REMARKS: Remarks begin on page 5 of this amendment.

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Sir/Ms.: